

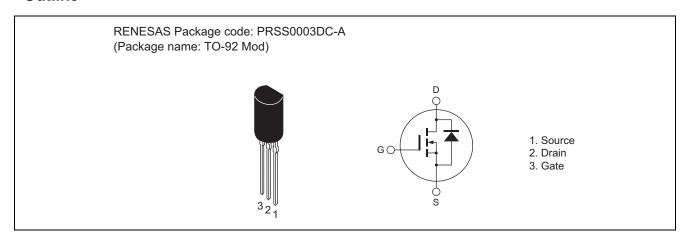
RJK6011DJE

600V - 0.1A - MOS FET High Speed Power Switching R07DS1153EJ0400 (Previous: REJ03G1577-0300) Rev.4.00 Jan 28, 2014

Features

- Low on-resistance $R_{DS(on)} = 35~\Omega~typ.~(at~I_D=0.05~A,~V_{GS}=10~V,~Ta=25^{\circ}C)$
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	600	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I _D	0.1	Α
Drain peak current	I _{D (pulse)} Note1	0.4	Α
Body-drain diode reverse drain current	I _{DR}	0.1	Α
Body-drain diode reverse drain peak current	I _{DR (pulse)} Note1	0.4	Α
Channel dissipation	Pch	0.9	W
Channel to ambient thermal impedance	$ heta_{ch-a}$	139	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \mu s$, duty cycle $\le 1\%$

Electrical Characteristics

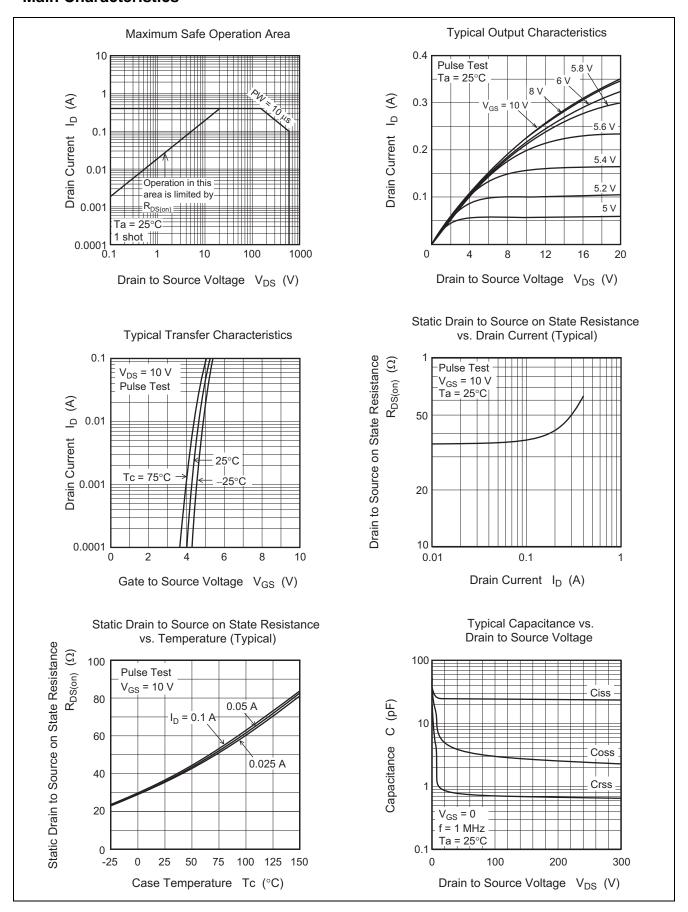
 $(Ta = 25^{\circ}C)$

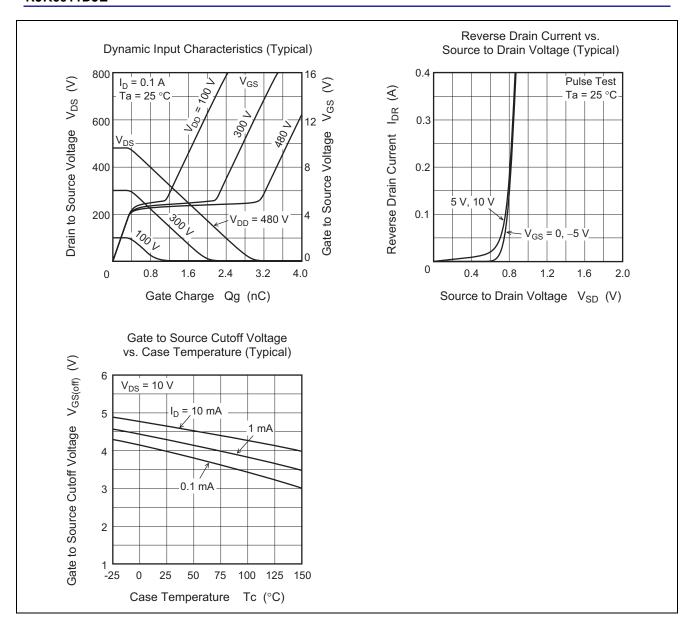
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	600	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 600 \text{ V}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	3	_	5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	35	52	Ω	$I_D = 0.05 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note2}}$
resistance						
Input capacitance	Ciss	_	25	_	pF	V _{DS} = 25 V
Output capacitance	Coss	_	4.7	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	0.9	_	pF	
Turn-on delay time	t _{d(on)}	_	33	_	ns	$I_D = 0.05 A$
Rise time	t _r	_	16	_	ns	$V_{GS} = 10 \text{ V}$ $R_L = 6000 \Omega$ $Rg = 10 \Omega$
Turn-off delay time	t _{d(off)}	_	54	_	ns	
Fall time	t _f	_	300	_	ns	
Total gate charge	Qg	_	3.7	_	nC	V _{DD} = 480 V
Gate to source charge	Qgs	_	0.4	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	_	2.7	_	nC	$I_D = 0.1 A$
Body-drain diode forward voltage	V_{DF}	_	0.80	1.35	V	$I_F = 0.1 \text{ A}, V_{GS} = 0^{\text{Note2}}$

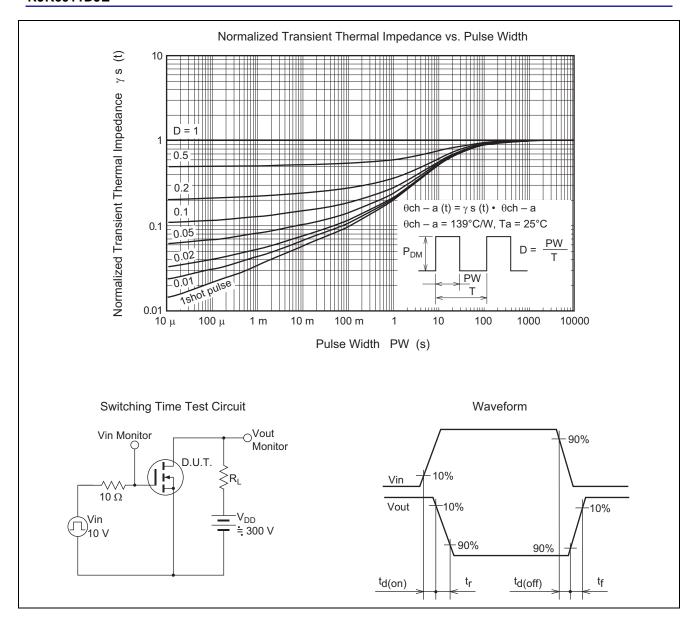
Notes: 2. Pulse test

- 3. Since this device is equipped with high voltage FET chip ($V_{DSS} \ge 600 \text{ V}$), high voltage may be supplied. Therefore, please be sure to confirm about Electric discharge between Drain terminal and other terminal.
- 4. This device is sensitive to electrostatic discharge. It is recommended to adopt appropriate cautions when handling this product.

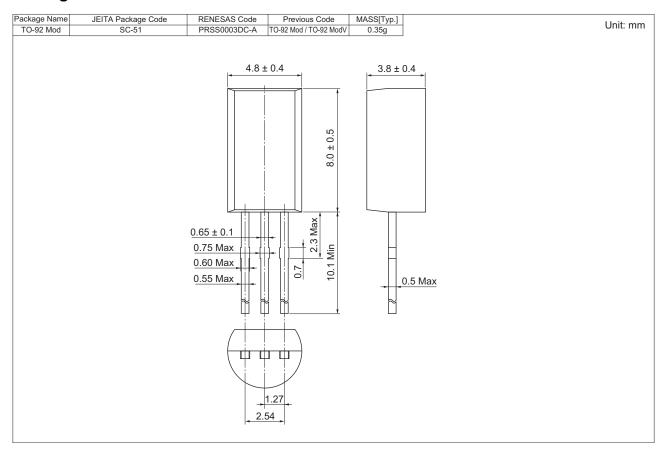
Main Characteristics







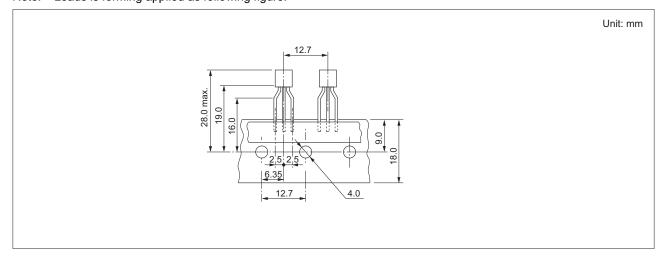
Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJK6011DJE-00#Z0	2500 pcs	Hold Box, Radial Taping

Note: Leads is forming applied as following figure.



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