



DMG8N65SCT

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on)	Package	Ι _D T _C = +25°C
650V	$1.3\Omega@V_{GS} = 10V$	TO220AB (Type TH)	8A

Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

Applications

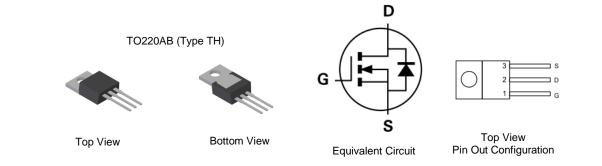
- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Features

- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TO220AB (Type TH)
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMG8N65SCT	TO220AB (Type TH)	50 Pieces/Tube

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

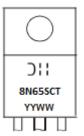
 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

Notes:

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



) | |=Manufacturer's Marking 8N65SCT = Product Type Marking Code YYWW = Date Code Marking YY or <u>YY</u> = Last Two Digits of Year (ex: 17 = 2017) WW or <u>WW</u> = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	650	V
Gate-Source Voltage			V _{GSS}	±30	V
Continuous Drain Current V _{GS} = 10V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ID	8.0 3.8	А
Maximum Body Diode Forward Current (Note 5)	Is	12	А		
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			IDM	12	А
Avalanche Current, L = 60mH (Note 7)			I _{AS}	3.6	А
Avalanche Energy, L = 60mH (Note 7)			E _{AS}	389	mJ
Peak Diode Recovery dv/dt			dv/dt	5	V/ns

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	T _C = +25°C	Р	125	W	
	T _C = +100°C	PD	50	vv	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	54	°C/W		
Thermal Resistance, Junction to Case	R ₀ JC	1			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

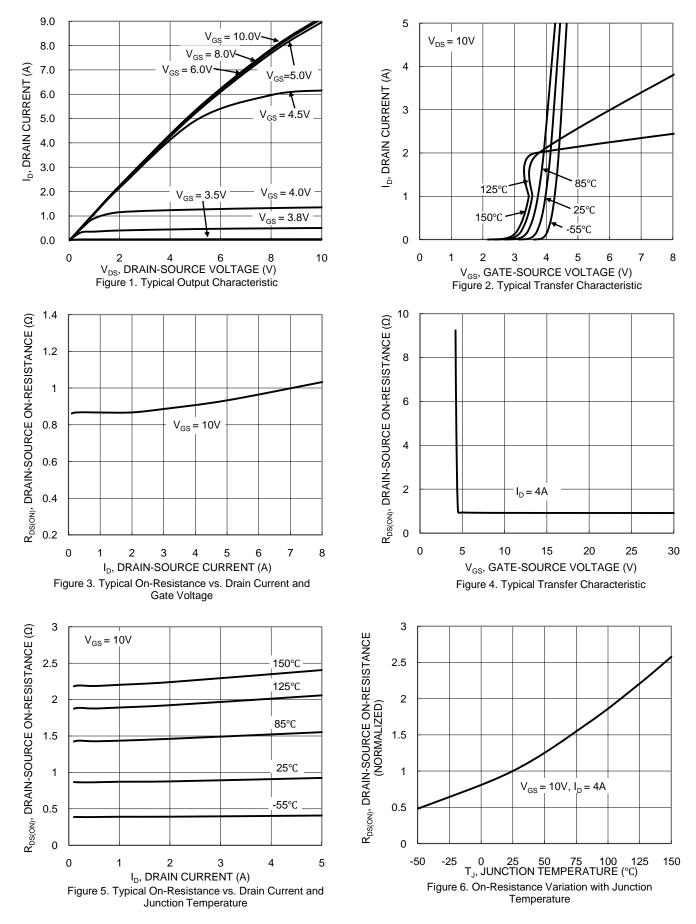
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	650	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 650 V, V_{GS} = 0 V$	
Gate-Source Leakage	I _{GSS}		_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	2	3	4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		0.9	1.3	Ω	$V_{GS} = 10V, I_D = 4A$	
Diode Forward Voltage	V _{SD}		0.87	1.5	V	$V_{GS} = 0V, I_{S} = 8A$	
DYNAMIC CHARACTERISTICS (Note 7)					-	-	
Input Capacitance	Ciss	_	1,217	_		$V_{DS} = 25V, f = 1.0MHz, V_{GS} = 0V$	
Output Capacitance	Coss		115		pF		
Reverse Transfer Capacitance	C _{rss}		12				
Gate Resistance	R _G		1.24	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg		30	_			
Gate-Source Charge	Q _{gs}		4.8	_	nC	V _{DD} = 520V, I _D =8A, V _{GS} = 10V	
Gate-Drain Charge	Q _{gd}	_	13.3			VGS = 10V	
Turn-On Delay Time	t _{D(ON)}	_	23			V _{DD} = 450V, R _G = 25Ω, I _D =8A, V _{GS} = 10V	
Turn-On Rise Time	t _R	_	46				
Turn-Off Delay Time	t _{D(OFF)}		115		ns		
Turn-Off Fall Time	tF	_	52				
Body Diode Reverse Recovery Time	t _{RR}		296		ns	di/dt = 100A/µs, V _{DS} = 100V,	
Body Diode Reverse Recovery Charge	Q _{RR}		2.7		μC	I _F = 8A	

Notes: 5. Device mounted on infinite heatsink.

Bevice mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Guaranteed by design. Not subject to production testing.
Short duration pulse test used to minimize self-heating effect.



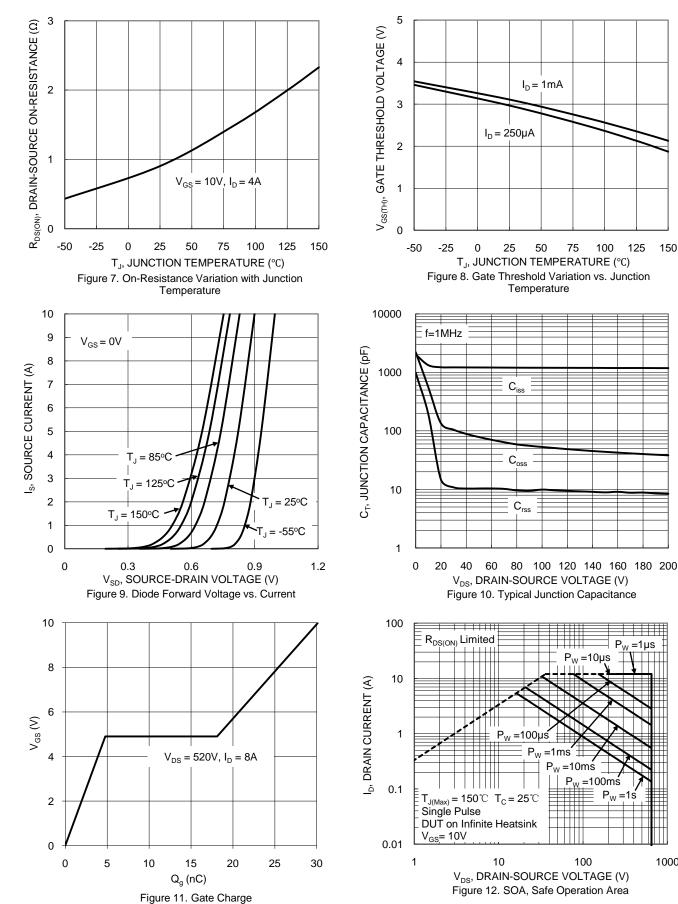
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DMG8N65SCT Document number: DS38402 Rev. 2 - 2

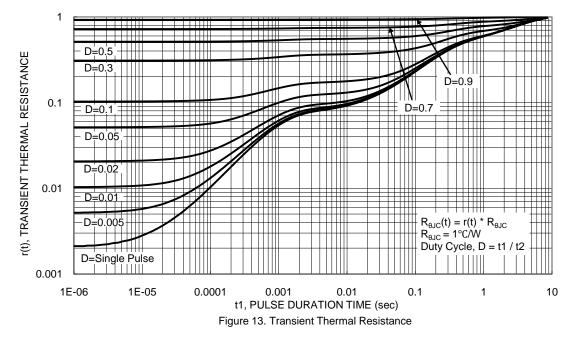


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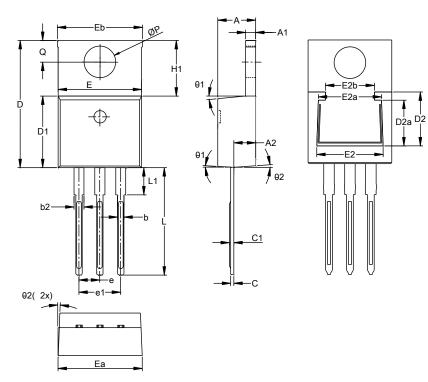






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



TO220AB (Type TH)

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Dim	Min	Max	Тур				
Α	4.27	4.87	4.57				
A1	1.12	1.42	1.27				
A2	2.39	2.99	2.69				
b	0.70	1.01	0.81				
b2	1.17	1.50	1.27				
С	0.30	0.53	0.38				
c1	0.38	0.72	0.56				
D	14.60	15.40	15.00				
D1	8.40	9.00	8.70				
D2	5.33	6.63	6.33				
D2a	4.54	5.84	5.54				
е	2.54 BSC						
e1	5.08 BSC						
E	9.88	10.50	10.16				
Ea	9.90	10.45	10.10				
Eb	9.90	10.65	10.25				
E2	7.06	8.36	8.06				
E2a	6.67	7.97	7.67				
E2b	4.94	6.24	5.94				
H1	5.70	6.65	6.30				
L	13.00	13.80	13.40				
L1	-	4.10	3.75				
Q	2.50	2.99	2.74				
ØP	3.70	3.99	3.84				
θ1	4°	10°	7°				
θ2	0° 6° 3°						
All Dimensions in mm							



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