# DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1999 May 28 2004 Nov 05



BC875; BC879

### **NPN Darlington transistors**

### FEATURES

- High DC current gain (min. 1000)
- High current (max. 1 A)
- Low voltage (max. 80 V)
- Integrated diode and resistor.

### APPLICATIONS

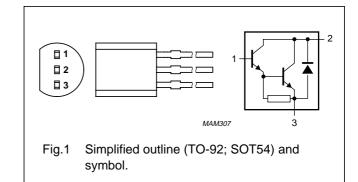
• Relay drivers.

### DESCRIPTION

NPN Darlington transistor in a TO-92 (SOT54) plastic package. PNP complement: BC878.

### PINNING

PIN	DESCRIPTION	
1	base	
2	collector	
3	emitter	



### **ORDERING INFORMATION**

		PACKAGE			
	NAME	DESCRIPTION	VERSION		
BC875	SC-43A	plastic single-ended leaded (through hole) package; 3 leads	SOT54		
BC879					

### BC875; BC879

### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BC875		-	60	V
	BC879		_	100	V
V <sub>CES</sub>	collector-emitter voltage	$V_{BE} = 0 V$			
	BC875		_	45	V
	BC879		_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		-	1	A
I <sub>CM</sub>	peak collector current -		-	2	A
I <sub>B</sub>	base current (DC)		-	0.2	A
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C; \text{ note } 1$	-	0.83	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

### Note

1. Transistor mounted on an FR4 printed-circuit board.

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	150	K/W

#### Note

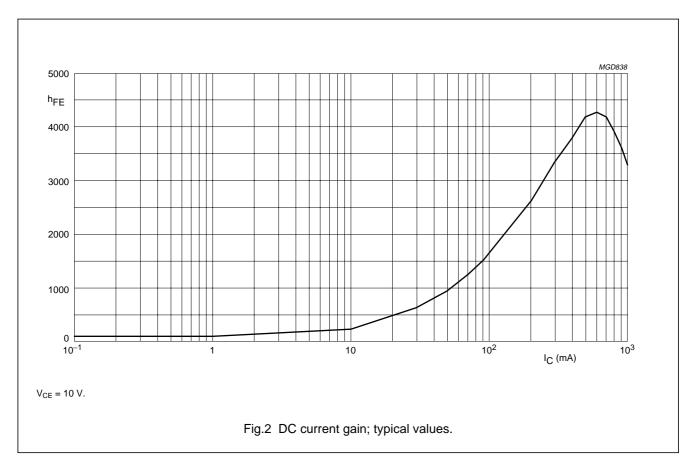
1. Transistor mounted on an FR4 printed-circuit board.

### BC875; BC879

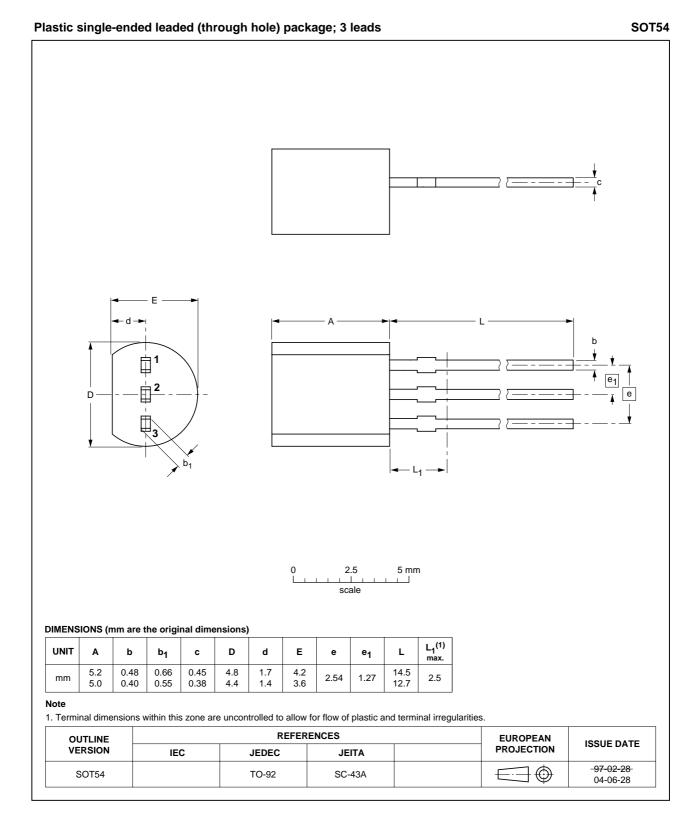
### CHARACTERISTICS

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CES</sub>	collector-emitter cut-off current	V <sub>BE</sub> = 0 V				
	BC875	V <sub>CE</sub> = 45 V	-	_	50	nA
	BC879	V <sub>CE</sub> = 80 V	-	-	50	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 4 V; I_{C} = 0 A$	-	-	50	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 10 V; see Fig.2				
		I <sub>C</sub> = 150 mA	1000	-	-	
		I <sub>C</sub> = 0.5 A	2000	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 0.5 A; I <sub>B</sub> = 0.5 mA	-	-	1.3	V
		I <sub>C</sub> = 1 A; I <sub>B</sub> = 1 mA	-	-	1.8	V
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 1 A; I <sub>B</sub> = 1 mA	-	-	2.2	V
f <sub>T</sub>	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 0.5 \text{ A}; f = 100 \text{ MHz}$	-	200	-	MHz
Switching times (between 10% and 90% levels)						
t <sub>on</sub>	turn-on time	I <sub>Con</sub> = 500 mA; I <sub>Bon</sub> = 0.5 mA;	-	500	-	ns
t <sub>off</sub>	turn-off time	$I_{Boff} = -0.5 \text{ mA}$		1300	-	ns



### PACKAGE OUTLINE



## BC875; BC879

BC875; BC879

### DATA SHEET STATUS

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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